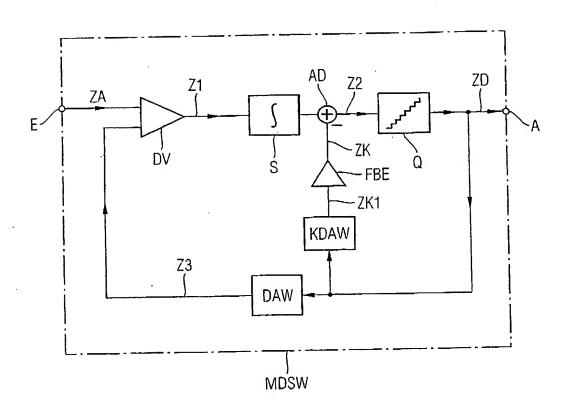
Title: POWER-SAVING MULTIBIT DELTA-SIGMA CONVERTER Applicant: Doerrer et al. Atty Docket: 1406/385

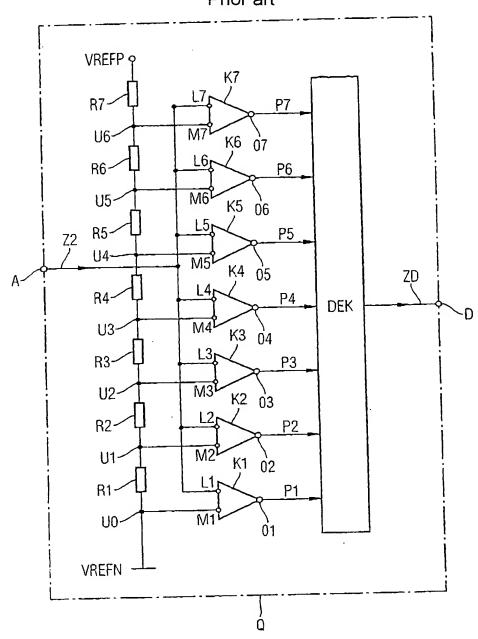
FIG 1 Prior art



Title: POWER-SAVING MULTIBIT DELTA-SIGMA CONVERTER

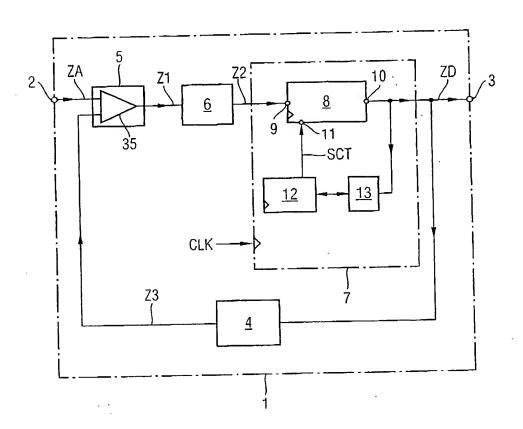
Applicant: Doerrer et al. Atty Docket: 1406/385

FIG 2 Prior art



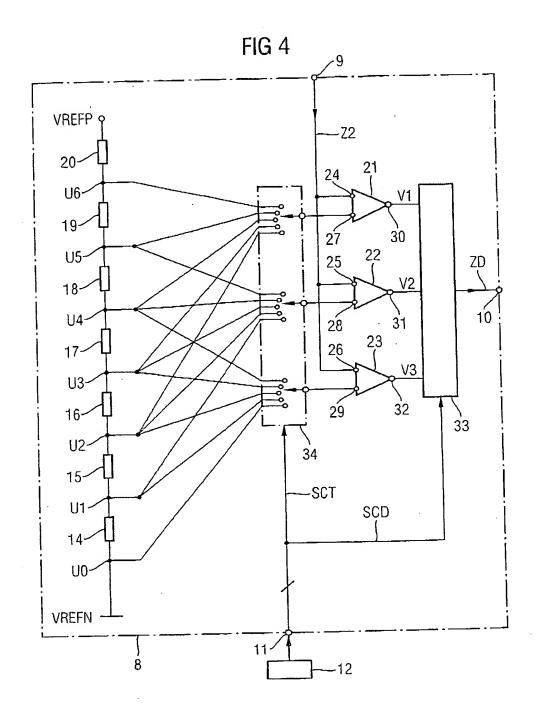
Title: POWER-SAVING MULTIBIT DELTA-SIGMA CONVERTER Applicant: Doerrer et al. Atty Docket: 1406/385

FIG 3



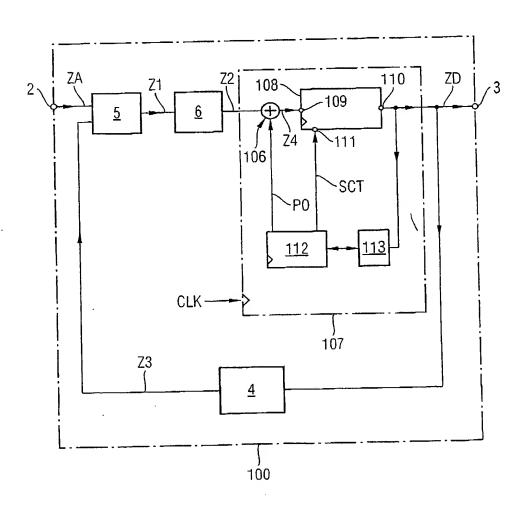
Title: POWER-SAVING MULTIBIT DELTA-SIGMA CONVERTER

Applicant: Doerrer et al. Atty Docket: 1406/385



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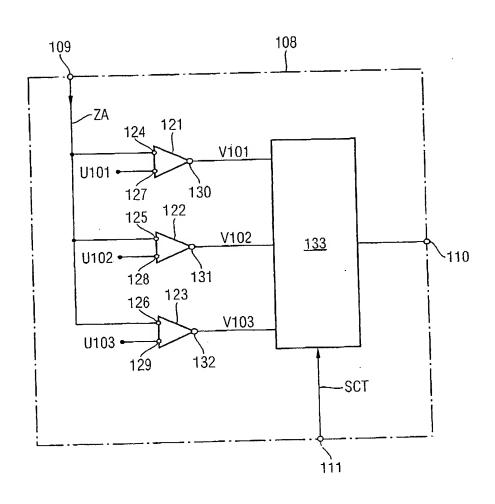
FIG 5



Title: POWER-SAVING MULTIBIT DELTA-SIGMA CONVERTER

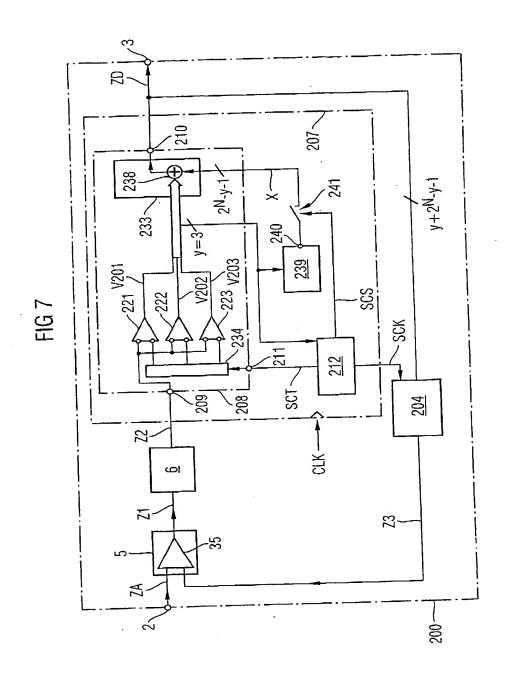
Applicant: Doerrer et al. Atty Docket: 1406/385

FIG 6



Title: POWER-SAVING MULTIBIT DELTA-SIGMA CONVERTER

Applicant: Doerrer et al. Atty Docket: 1406/385



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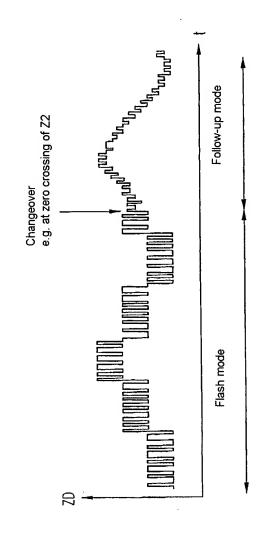
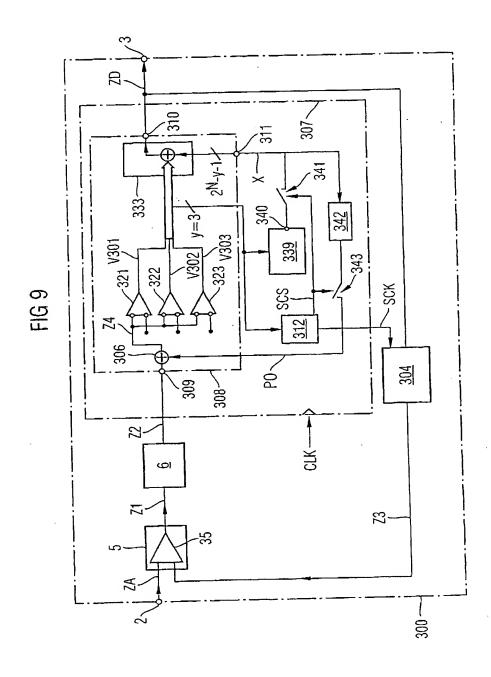
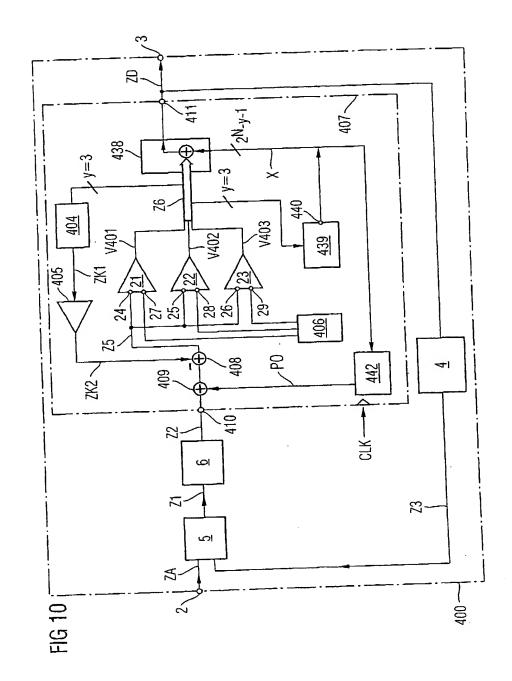


FIG 8

Title: POWER-SAVING MULTIBIT DELTA-SIGMA CONVERTER Applicant: Doerrer et al. Atty Docket: 1406/385



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